



N-CHANNEL ENHANCEMENT MOSFET

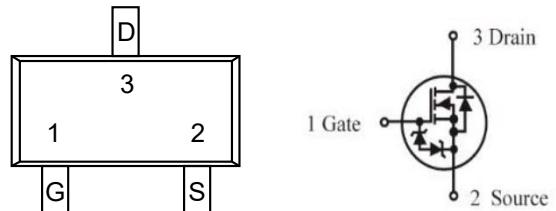
VDS= 50V, ID= 200mA



FEATURES

- ◆ We declare that the material of product compliance with RoHS requirements and Halogen Free.
- ◆ Low threshold voltage ($V_{GS(th)}$): 0.5V...1.5V) makes it ideal for low voltage applications
- ◆ ESD Protected : 1500V
- ◆ Marking : J2

SOT-23



Maximum Ratings @ TA=25°C unless otherwise specified

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	50	V
Gate-to-Source Voltage-Continuous	V_{GSS}	± 20	V
Drain Current	I_D	200	mA
	I_{DM}	800	mA
Total Device Dissipation	P_D	225	mW
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	°C
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	T_L	260	°C

Electrical Characteristics @ TA=25°C unless otherwise specified, per element

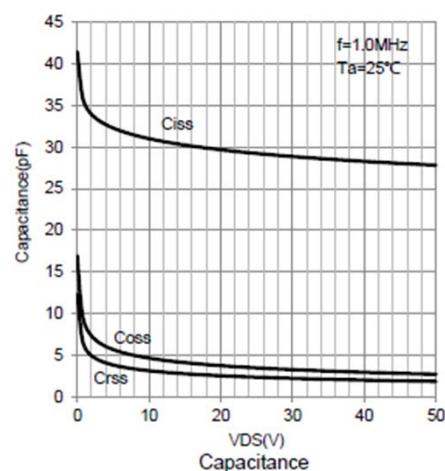
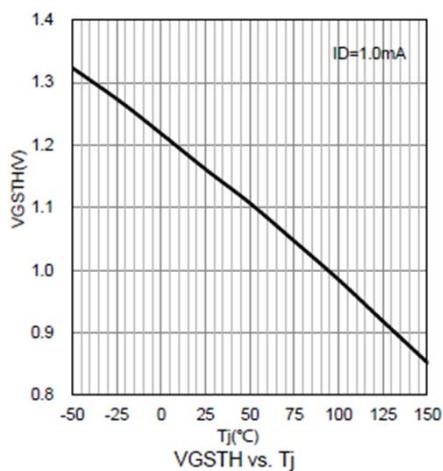
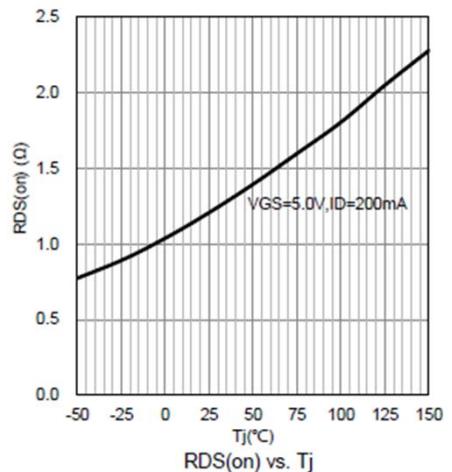
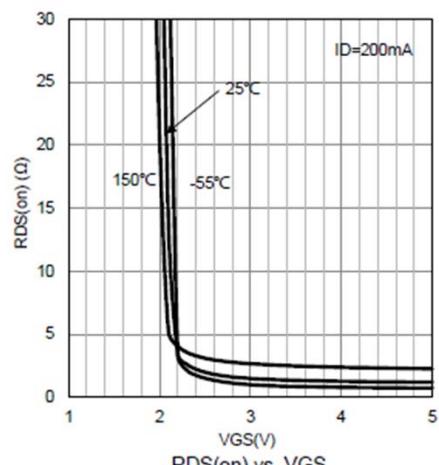
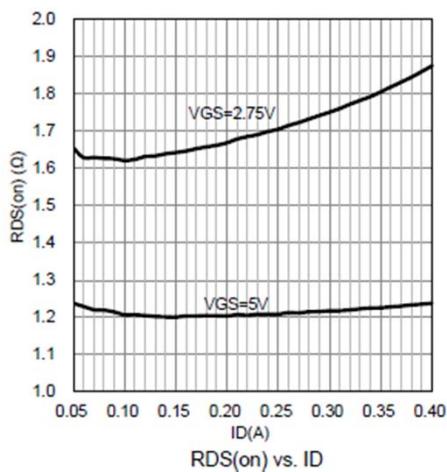
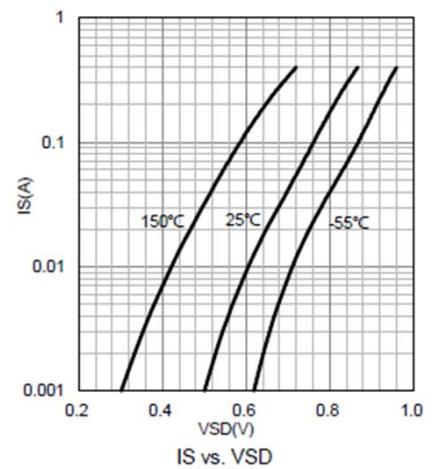
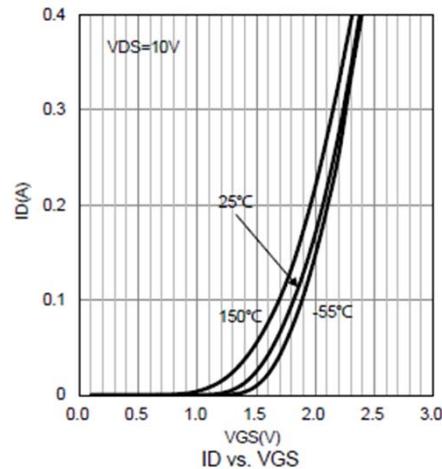
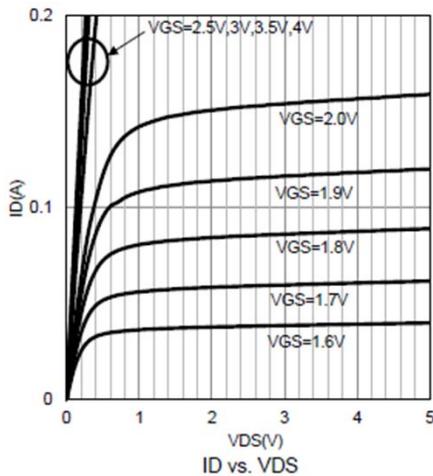
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	50	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	0.1	uA	$V_{DS}=25V, V_{GS}=0V$
		-	-	0.5		$V_{DS}=50V, V_{GS}=0V$
Gate-source Leakage	I_{GSS}	-	-	± 10	uA	$V_{GS}=\pm 20V$
ON CHARACTERISTICS (NOTE 1)						
Gate Threshold Voltage	$V_{GS(th)}$	0.5	-	1.5	V	$V_{DS}=V_{GS}, I_D=1mA$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	-	3.5	Ω	$V_{GS}=5V, I_D=200mA$
Forward Transconductance	g_{fs}	100	-	-	mS	$V_{DS}=25V, I_D=200mA$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	-	22.8	-	pF	$V_{DS}=25V$
Output Capacitance	C_{oss}	-	3.5	-	pF	$V_{GS}=0V$
Reverse Transfer Capacitance	C_{rss}	-	2.9	-	pF	f=1.0MHz
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$T_{D(ON)}$	-	3.8	-	nS	$I_D=500mA, V_{DD}=30V$
Turn-Off Delay Time	$T_{D(OFF)}$	-	19	-	nS	$V_{GEN}=10V, R_L=60\Omega, R_G=25\Omega$

NOTES :

- 1.Pulse Test : Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2.0\%$.

DEVICE CHARACTERISTICS

BSS139



PACKAGE OUTLINE & DIMENSIONS

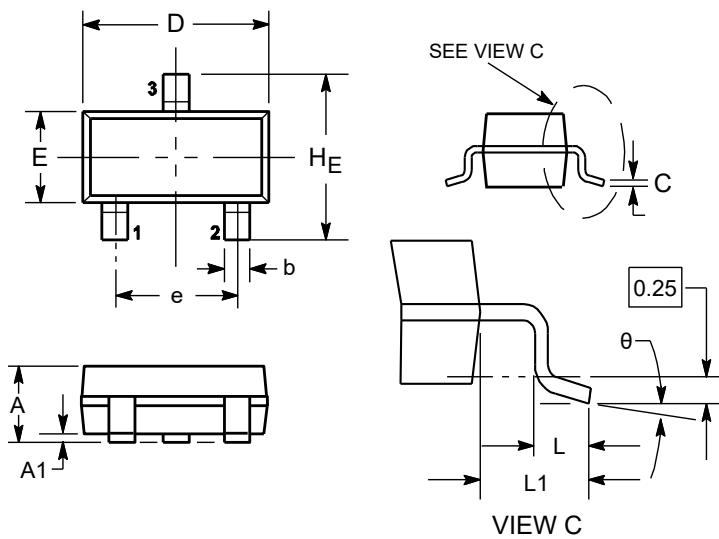
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Notes:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1	1.11	0.035	0.04	0.044
A1	0.01	0.06	0.1	0.001	0.002	0.004
b	0.37	0.44	0.5	0.015	0.018	0.02
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.9	3.04	0.11	0.114	0.12
E	1.20	1.3	1.4	0.047	0.051	0.055
e	1.78	1.9	2.04	0.07	0.075	0.081
L	0.10	0.2	0.3	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
H _E	2.10	2.4	2.64	0.083	0.094	0.104
θ	0°	---	10°	0°	---	10°



Soldering Footprint

